

Nanodsp Datasheet

input channels : 2
output channels: 4

inputs XLR balanced mono, cinch stereo

outputs XLR input balanced loopthrough
2x XLR balanced out
4x cinch
2x 3pol screw terminal

supply 5-12V screwterminal
programming 10p socket USBi standard

signal levels

input 2V rms /ch in (4V rms, +12dBV balanced in)

output 2V rms out +6dBV p[er channel; 4V rms +12dBV balanced out

features:

- crystal clock 12.288 Mhz: 48khz, 96khz or 192khz wordclock.
- ADAU1701 50MIPS dsp
- Output mute during startup, undervoltage (vsupply < 4.3V)
- NPO capacitors in faseliner DAC filters
- provision for control pot for volume
- provision for bicolor led for signal indication
- built and programmed with testprogram to generate sinewaves
- S/N see ADAU1701 spec sheet
- programming : Sigmastudio from analog devices
- applications: stereo X-over 2-way, mono X-over 4-way. Balanced X-over 2-way , compressor, limiter, delay etc.